



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ACTION TAKEN REPORT - FEEDBACK ANALYSIS ON CURRICULUM DEVELOPMENT

Programme Name: M. TECH - ECE

Year of Revision: 2018

Sl.No	Name	Stake Holder	Feedback from Stakeholder	Action Taken
1	Mr. Nadarajan Supervisor - Private Company Ward Name - Praveen	Parent	Improve more practical knowledge, Industrial visits. Training for placement required.	Included in curriculum 2018
2	Mr.Radhesyam Pandit Manager - Private employee Ward Name - Pankaj Kumar	Parent	Training on soft skills and placement trainings are required	Included in curriculum 2018
3	Mr.Shaji Government Employee Ward Name - Shaicy Shaji	Parent	To conduct more seminars	Included in assessment pattern in curriculum
4	Mr.Mathew Entrpreneur (Rubber Dealer) Ward Name - Rincy Juliet	Parent	More placement related courses for PG required	Incorporated in elective courses
5	Mr.Kolanji Teacher Ward Name - Santhosk K	Parent	Core Companies skill based training can be given	Comprehension course included in UG level
6	Mr.Victor Business Ward Name - Dr.Sharmila	Parent	Research Components can be more focused	Publication of project works are encouraged among students in curriculum 2018
7	Dr.Rajesh Shyamala Devi AP(SG)	Faculty	Recent advancement on 4G technology can be included into curriculum	Suggested topics are included in mobile communication courses
X	Dr.Aby K Thomas Professor	Faculty	Recent topics on Wireless adhoc networks can be included	Included in curriculum 2018

9	Dr.Punithavathy Mohan AP(SG)	Faculty	Optimization techniques can be included in elective courses or as a module in image processing techniques	Suggested topics are included in elective courses
10	Dr.M.Rajmohan AP(SG)	Faculty	Network on Chip.SoC can be included	Incorporated in embedded systems
11	Dr.S.Sunil Kumar AP(SG)	Faculty	Interdisciplinary courses can be offered	Included in Non-department elective courses .
12	Dr.AL Vallikannu Professor	Faculty	Simulations for circuits, antennas can be included	Incorporated circuit simulation based courses.
13	Ms.Devi Priya 2013-2016 M.Tech - CS	Student	Internship can be added as a course with credits	Incorporated at UG level
14	Parameshwar 2015-2017 M.Tech - VLSI	Student	Lab integrated course can be added	Presented in CRC meeting and design projects are included from semester 3
15	Dany George 2015-2017 M.Tech - CS	Student	Soft skills training can be included	Included in curriculum 2018
16	Jitin James 2012-2014 M.Tech Embedded Systems	Student	Comprehension course would be given for preparing for competitive exam	Incorporated at UG level
17	Ranjith Kumar 2012-2014 M.Tech Embedded Systems	Student	Machine Learning can be introduced	Included in curriculum 2018 at under graduate level
18	Raghunath Pentyala 2012-2014 M.Tech Embedded Systems	Student	Focus on research skills can be improved	Publication of Projects works are encouraged among PG students.
19	Mr.Naveen Kolla Technical Recruiter Kaztronix LLC	Alumni	Include latest programming languages	Programming and simulation related papers are included/revised.
20	Jubin Sebastin Research Engineer, ivESK, Hochschule offenberg, Germany	Alumni	IOT ,Cyber Security and 5G courses are needed	This is made compulsory

Venkatasubramanian Ramakrishnan Head - Cyber Security Consulting S.U.Maheswar Rao Member of ministry, Fisheries Department Vamsi Krishna Jagarlamudi Principal Software Engineer at Gigamon Dr.S.K.Srivatsa Technical Lead manager HP India PVT LTD Mr.Karthikeyan Software Analyst Wipro Technologies Mr.S.Thilagavathy Service Inspection Engineer - Networks 5G Tech and Consultant Mr.Shiv Shankar Project Manager Software Testing Deloitte Dr.S.Saurabh Saxena Team Lead Program Analyst NVIDIA Cloud integration, Database integration for all ship-based technology, processing to be in memory database, automation of computer development & presented in CRC meeticonsideration. Presented in CRC meeticonsideration. Presented in CRC meeticonsideration. Included in curriculum2 International authors can be recommended PG students Employer Networks, Cyber Security, Networks, Cyber Security, Networks security can be included Incorporated in elective Subjects like Digital switching, digital transmission, telecom infrastructure, broadband systems can be included in curriculum Mr.Shiv Shankar Project Manager Software Testing Deloitte Dr.Saurabh Saxena Team Lead Program Analyst NVIDIA Employer FETE amplifier configurations (CS, CG and CD) ring oscillator can be included. Concept of bandgap for generating reference voltages can be included. Presented in CRC meeticonsideration.				
22 Ramakrishnan Head - Cyber Security Consulting S.U.Maheswar Rao Member of ministry, Fisheries Department Vamsi Krishna Jagarlamudi Principal Software Engineer at Gigamon Dr.S.K.Srivatsa Technical Lead manager HP India PVT LTD Mr.Karthikeyan Software Analyst Wipro Technologies Mr.S.Thilagavathy Service Inspection Engineer — Networks 5G Tech and Consultant Mr.Shiv Shankar Project Manager Software Testing Deloitte Dr.Saurabh Saxena Team Lead Program Analyst NVIDIA Alumni Syllabus and assessment pattern can be modified Syllabus and assessment pattern can be modified Consideration. Presented in CRC meetic consideration. Presented in	/ I I '	Alumni	Embedded System can be included	Included in B.Tech Curriculum 2018
Member of ministry, Fisheries Department Department	22 Ramakrishnan	Alumni	ship-based technology, processing to be in memory database, automation of computer development &	
Principal Software Engineer at Gigamon Dr.S.K.Srivatsa Technical Lead manager HP India PVT LTD Mr.Karthikeyan Software Analyst Wipro Technologies Mr.S.Thilagavathy Service Inspection Engineer – Networks 5G Tech and Consultant Mr.Shiv Shankar Project Manager Software Testing Deloitte Dr.Saurabh Saxena Dr.Saurabh Saxena Team Lead Program Analyst NVIDIA More practical courses can be included Included in curriculum2 Recommended and imp PG curriculum Recommended and imp PG curriculum Recommended and imp PG curriculum Recommended in curriculum Recommended in curriculum Incorporated in elective displayed in curriculum Course on analog and digital IC design can be included. Wireless system design can be included in curriculum 2 Team Lead Program Analyst NVIDIA More practical courses can be included Included in curriculum2 Cognitive Networks, Cyber Security, Network security can be included in curriculum Subjects like Digital switching, digital transmission, telecom infrastructure, broadband systems can be included in curriculum Course on analog and digital IC design can be included. Wireless system design can be included. Wireless system design can be included. The commended and imperson in person in person in person in propertion in propertion in propertion in person in syllage. Team Lead Program Analyst NVIDIA Presented in CRC meeting bandgap for generating reference voltages can be included.	23 Member of ministry, Fisheries	Alumni	Syllabus and assessment pattern can be modified	Presented in CRC meeting for consideration.
Technical Lead manager HP India PVT LTD Mr.Karthikeyan Software Analyst Wipro Technologies Mr.S.Thilagavathy Service Inspection Engineer – Networks 5G Tech and Consultant Mr.Shiv Shankar Project Manager Software Testing Deloitte Dr.Saurabh Saxena Team Lead Program Analyst NVIDIA Employer International authors can be recommended PG students International authors can be recommended PG students Femployer International authors can be recommended PG students International authors can be recommended and impercent processes International authors can be included Incorporated in certification processes as topics in syllater processes as topics in syllater processes as topics in sylla	24 Principal Software Engineer at	Alumni	More practical courses can be included	Included in curriculum2018
Software Analyst Wipro Technologies Mr.S.Thilagavathy Service Inspection Engineer – Networks 5G Tech and Consultant Mr.Shiv Shankar Project Manager Software Testing Deloitte Dr.Saurabh Saxena Team Lead Program Analyst NVIDIA Employer To Subjects like Digital switching, digital Lead Program Analyst Subjects like Digital switching, digital Lead Digital Switching,	25 Technical Lead manager	Employer		Recommended and implemented in PG curriculum
Service Inspection Engineer – Networks 5G Tech and Consultant Mr.Shiv Shankar Project Manager Software Testing Deloitte Dr.Saurabh Saxena Team Lead Program Analyst NVIDIA Employer transmission, telecom infrastructure, broadband systems can be included in curriculum Course on analog and digital IC design can be included. Wireless system design can be included FETE amplifier configurations (CS, CG and CD) ring oscillator can be included. Concept of bandgap for generating reference voltages can be included Presented in CRC meeting presented in CRC meeting and digital IC design can be included. Concept of bandgap for generating reference voltages can be included.	26 Software Analyst	Employer	IOT, Cognitive Networks, Cyber Security, Network security can be included	Incorporated in elective courses
Mr.Shiv Shankar Project Manager Software Testing Deloitte Employer Dr.Saurabh Saxena Team Lead Program Analyst NVIDIA Employer Team Lead Program Analyst NVIDIA Team Lead Program Analyst NVIDIA Employer Course on analog and digital IC design can be included Employer FETE amplifier configurations (CS, CG and CD) ring oscillator can be included. Concept of bandgap for generating reference voltages can be included	27 Service Inspection Engineer –	Employer	transmission, telecom infrastructure, broadband	Incorporated in communication base courses as topics in syllabus
Team Lead Program Analyst NVIDIA Employer bandgap for generating reference voltages can be included Presented in CRC meeting reference voltages can be included	28 Project Manager Software Testing	Employer	Course on analog and digital IC design can be	Included in curriculum 2018
Ms Sheela Krishna Swami	29 Team Lead Program Analyst NVIDIA	Linployer	ring oscillator can be included. Concept of bandgap for generating reference voltages can be	Presented in CRC meeting
Comprehensive viva should be a part of the	C	Employer	Comprehensive viva should be a part of the curriculum to have strong fundamentals	Included in undergraduate level

Coordinator (BoS) Ms.G.Vimalarani HoD Off 03/2023

Chairman (BoS)
Dr.AL Vallikannu)